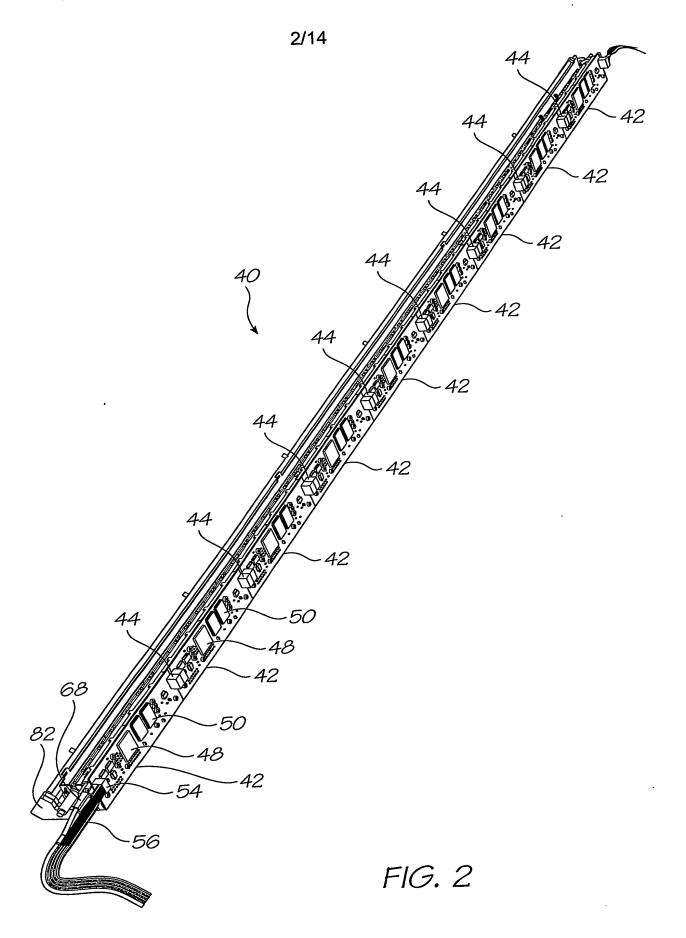
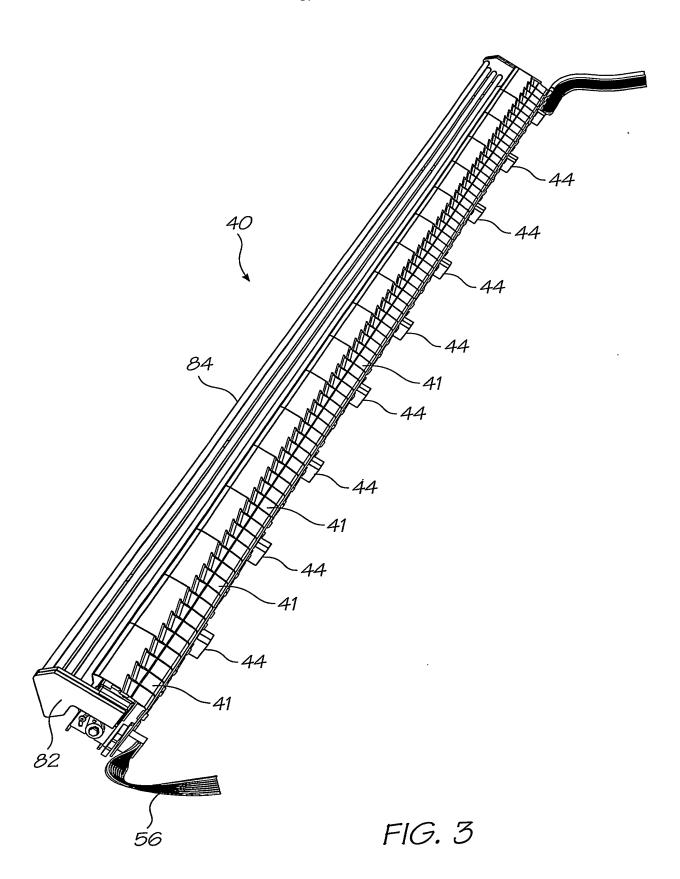
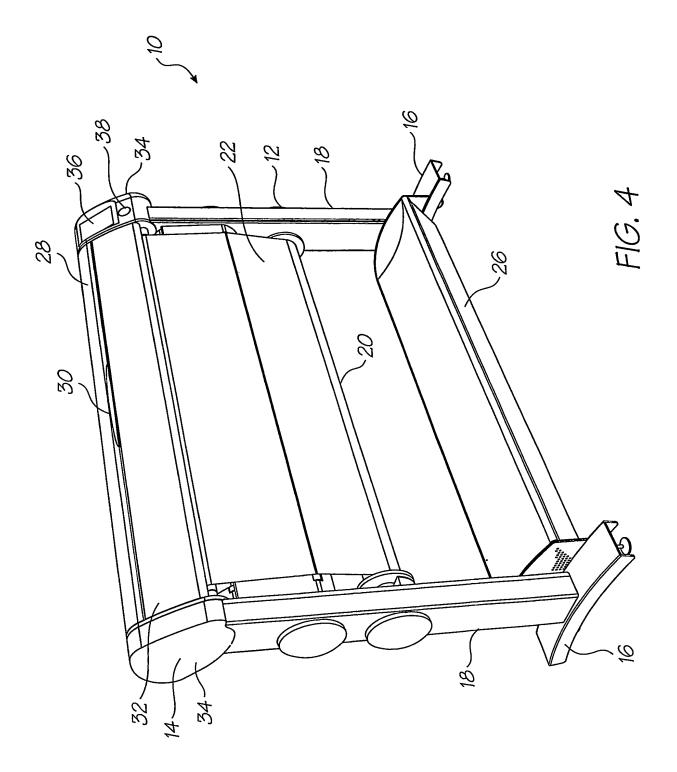


FIG. 1







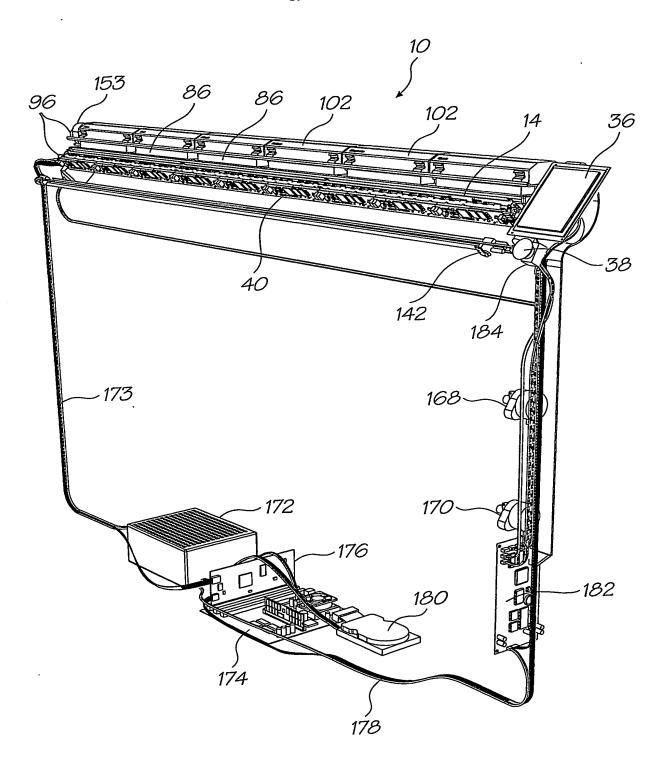


FIG. 5

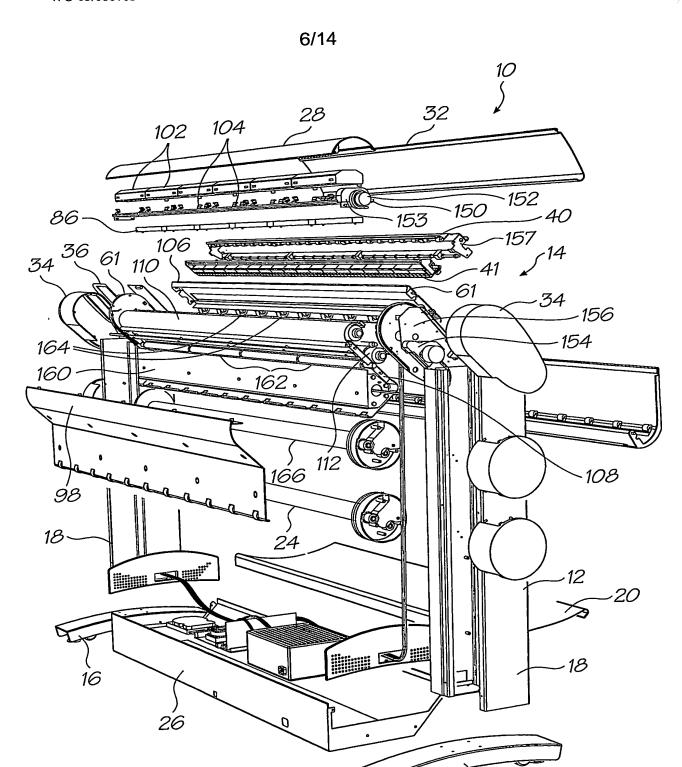


FIG. 6

16

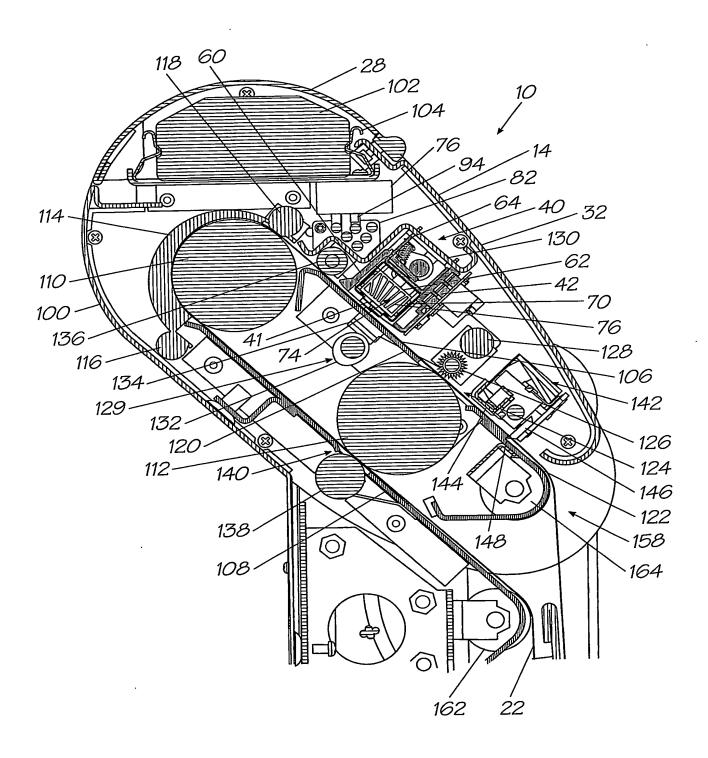
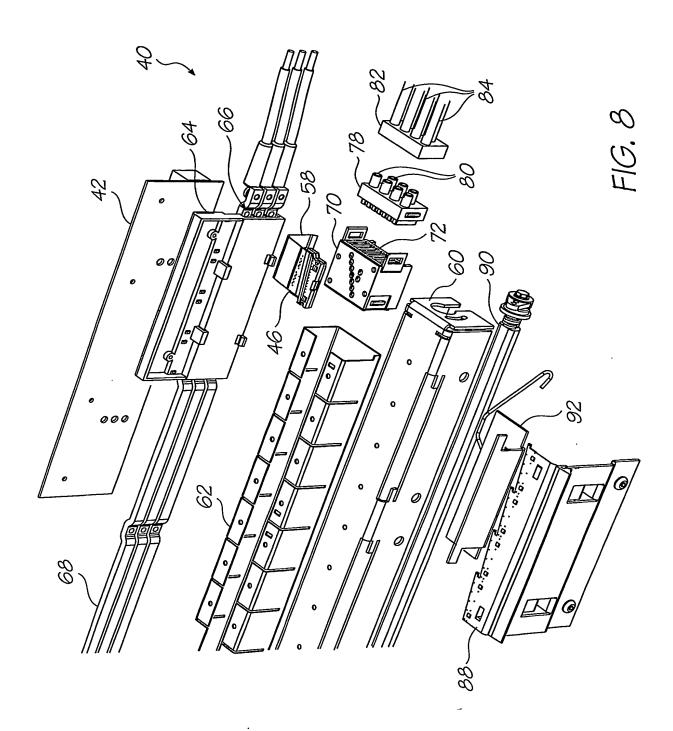
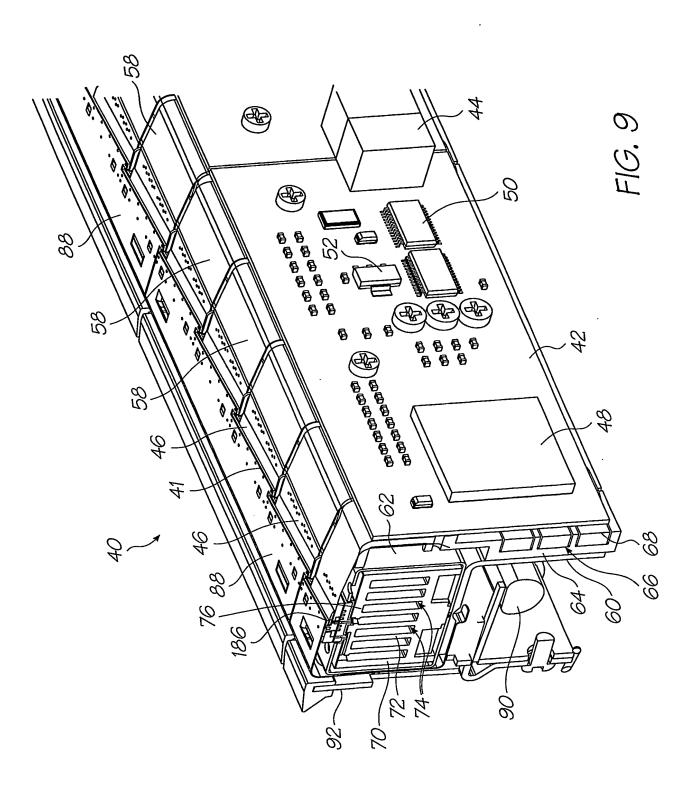
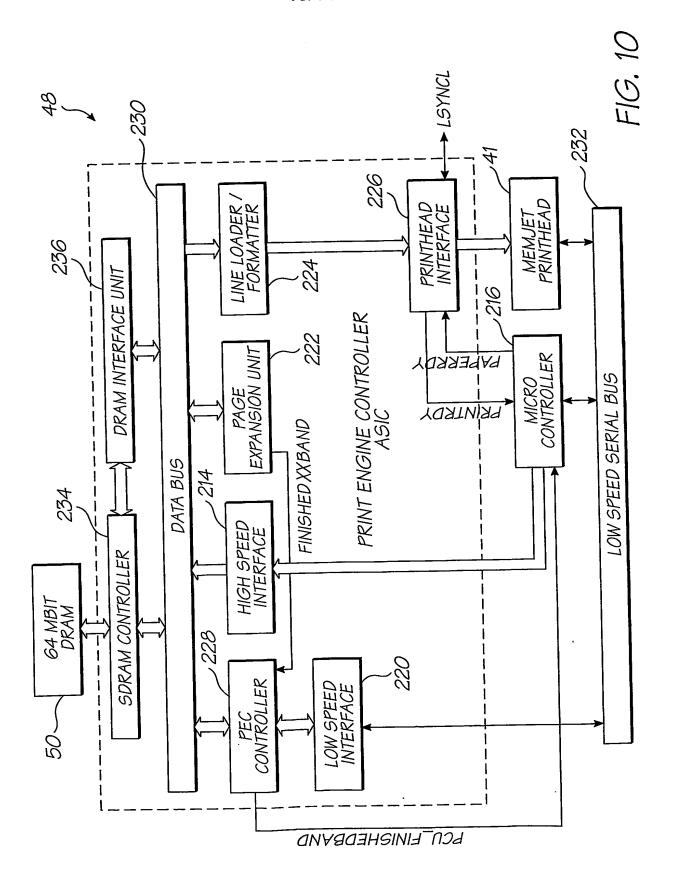


FIG. 7





10/14



230

DRAM INTERFACE

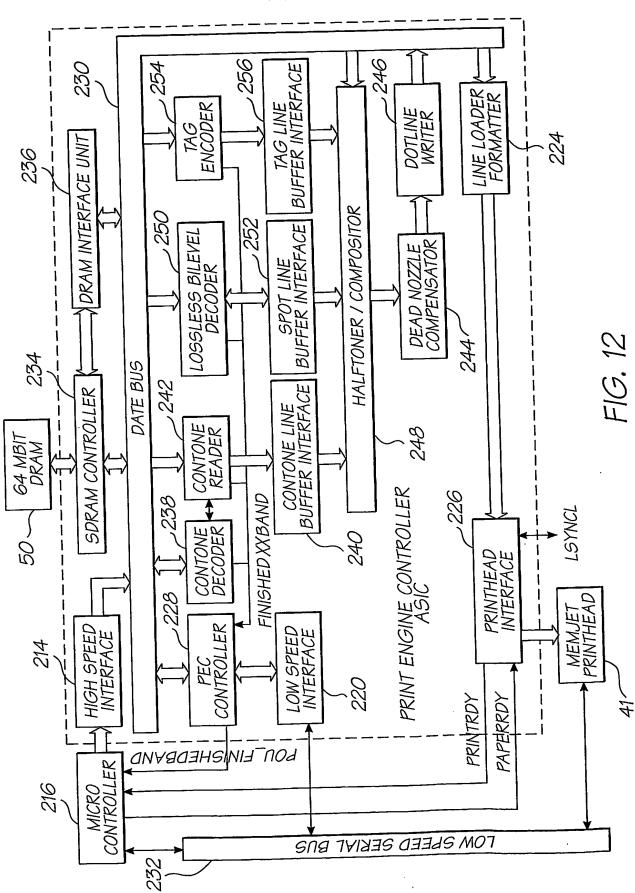
254

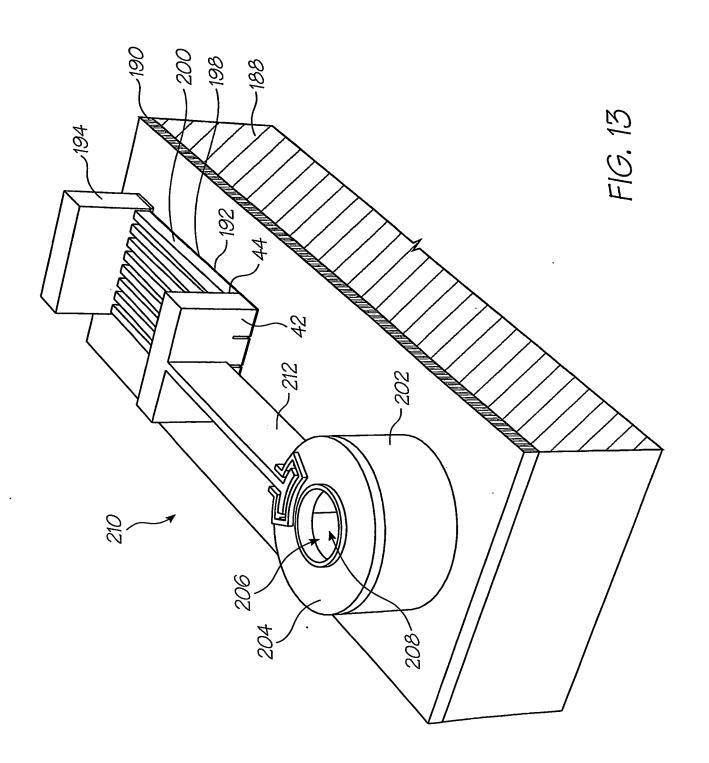
.222

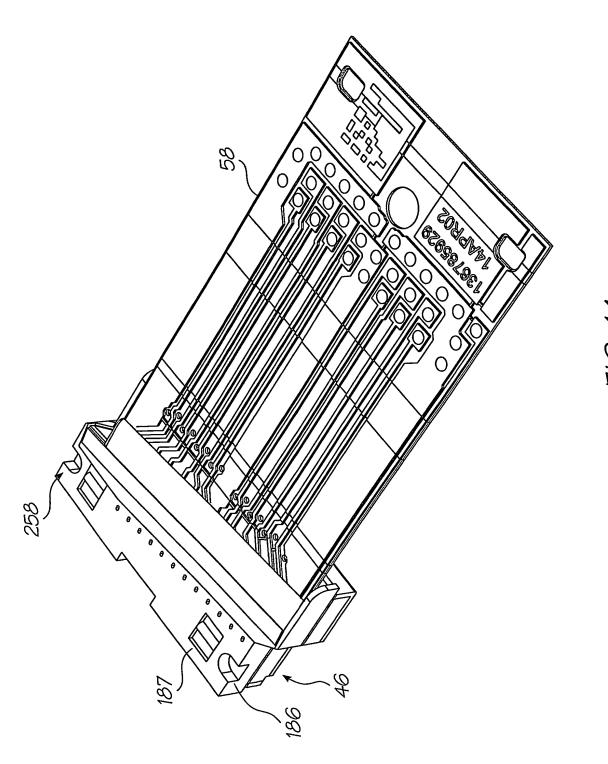
250

238~

DATE BUS







F/0.74